

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Wayne F. Ellis et al.

Serial No.: 10/707,797

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Title: Integrated Redundancy Architecture And Method for Providing Redundancy Allocation
to an Embedded Memory System

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Examiner: Guerrier Merant

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 15, 2007

RESPONSE TO FIRST OFFICE ACTION

This is in response to the Office Action mailed from the United States Patent and Trademark Office on August 17, 2007, with respect to the above-identified application. A Status of the Claims and Remarks concerning the Office Action start, respectively, on the following pages 2 and 6.